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File 347: JAPIO Nov 1976-2003/Nov(Updated 040308)
         (c) 2004 JPO & JAPIO
File 350: Derwent WPIX 1963-2004/UD, UM & UP=200419
         (c) 2004 Thomson Derwent
File 371: French Patents 1961-2002/BOPI 200209
         (c) 2002 INPI. All rts. reserv.
                Description
        Items
Set
                COUNTDOWN OR (COUNT??? OR NUMERAT??? OR ENUMERAT??? OR CLO-
        17979
S1
             CK???) (3N) (DOWN OR BACK? OR REVERSE)
               (TIME OR TERM OR PERIOD OR MINUTE? ? OR SECOND? ?) (3N) (LEFT
S2
              OR REMAIN??? OR LIMIT? ?) OR DEADLINE OR END OR CLOSE? ? OR -
             OVER
                COMPAR? OR SYNCHRONI? OR SYNC? ? OR SYNCH? ? OR CORRELAT???
S3
      2532613
              OR CORELAT??? OR RELATE? ? OR ADJUST? OR MATCH???
               CLOCK OR TIME OR TIMER OR TIMEKEEPER OR CHRONOGRAPH OR CHR-
             ONOSCOPE OR CHRONOMETER OR TIMEPIECE OR HOROLOGE
               SERVER OR AUCTION? OR REMOTE?? OR FILESERVER? ? OR WEBSERV-
$5
             ER? ? OR OUTSIDE OR OUT()SIDE OR (ANOTHER OR OTHER OR DIFFERE-
             NT OR DISTAN?? OR OFF)()(LOCATION OR SITE OR PLACE) OR ELSE()-
             WHERE OR ELSEWHERE OR CENTRAL?? OR OFFSITE
                INTERNAL OR LOCAL OR PERSONAL OR PC OR USER? ? OR BUYER? ?
S6
             OR BIDDER? ? OR PATRON? ? OR CUSTOMER? ? OR CLIENT? ? OR SHOP-
             PER? ? OR DRIVE? ? OR HARDDRIVE? ? OR CPU? ? OR ONSITE OR ON(-
             )SITE OR INHOUSE OR IN()HOUSE
          697
                S1(10N)S2
S7
       130939
                S3(5N)S4
S8
                S8(10N)(S5 OR S6)
         8800
S 9
                S7(S)S9
S10
            0
          769
                S9(S)(S1 OR S2)
S11
                IC=(G06F-017? OR G06F-001? OR G06F-013? OR H04L-005? OR H0-
S12
       511,226.
             4L-007? OR G04C-011? OR G04C-013?)
          159
                S11 AND S12
S13
S14
          314
                S9(10N)(S1 OR S2)
                S12 AND S14
S15
           66
            5
                S9(10N)(S1 AND S2)
S16
                S9 AND (S1 AND S2)
           11
S17
               _S12_AND_S17_
S18
           _4_
                S16 OR S18
S19
            9
                IDPAT (sorted in duplicate/non-duplicate order)
S20
               IDPAT (primary/non-duplicate records only)
S21
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?show files;ds

(Item 3 from ! 21/3,K/3 DIALOG(R) File 350: Derwent WPIX (c) 2004 Thomson Derwent. All rts. reserv. **Image available** 008173654 WPI Acc No: 1990-060655/199009 XRPX Acc No: N90-046584 Synchronisation of audio-video optical disc systems - has local clock slaved to video packet clock by separate loops providing first lock in frequency then holding lock in phase Patent Assignee: PHILIPS GLOEILAMPENFAB NV (PHIG); US PHILIPS CORP (PHIG Inventor: HORSTMAN R; SCHRUERS J; SCHREURS J; HORSTMAN R A; SCHREURS J C Number of Countries: 007 Number of Patents: 006 Patent Family: Patent No Kind Date Applicat No Kind Date Week 19890817 199009 EP 89202100 **EP** 355919 19900228 Α Α 199015 Α 19900223 FR 2635623 JP 89211534 19890818 19900601 Α 199028 JP 2143958 Α EP 89202100 EP 355919 В1 19940209 Α 19890817 199406 DE 612990 19890817 DE 68912990 Ε 19940324 Α 199413 EP 89202100 19890817 Α US 89395126 19890817 199550 US 5465277 19951107 Α US 91651116 19910205 Α Priority Applications (No Type Date): FR 8811034 A 19880819 Patent Details: Filing Notes Patent No Kind Lan Pg Main IPC A F 11 EP 355919 Designated States (Regional): AT DE FR GB IT EP 355919 ' B1 F 14 H04N-005/93 Designated States (Regional): AT DE FR GB IT H04N-005/93 Based on patent EP 355919 DE 68912990 Ε US 5465277 Cont of application US 89395126 8 H03D-003/24 Α ... Abstract (Basic): audio element clock signal (H F!). Thereafter, the video packet frequency (B FO!) signal takes *over* and maintains control of the oscillator's phase... ... Abstract (Equivalent): Device for the *synchronization* of a *local* *clock* signals with a first clock frequency which is that of an information signal comprising digital... ... Abstract (Equivalent): 1. A device for the *synchronization* of a *local* *clock* signal with an information signal comprising a number of time spaced digital data bursts having... ...continuous signal derived from the reference signal, the frequency comparator comprising a counter having a *countdown* input for receiving the local clock signal and a control input for receiving a ...International Patent Class (Additional): *H04L-007/06* (Item 7 from file: 350) 21/3, K/7DIALOG(R) File 350: Derwent WPIX (c) 2004 Thomson Derwent. All rts. reserv. 004130752 WPI Acc No: 1984-276292/198445

XRPX Acc No: N84-206195

Phase locking device in switching system - has reference clock and phase error monitor using local and main clock signal

Patent Assignee: GTE COMMUNICATION SYSTEM CORP (SYLV Inventor: EDWARDS I L; MACRANDER M S; MCLAUGHLIN R C Number of Countries: 004 Number of Patents: 004

_T __ & __ & __ . The had

Patent Family:

17 ×

Date Applicat No Kind Date Week Patent No Kind